



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

KIMURA et al

Atty. Ref.: 1035-471; Confirmation No. 4479

Appl. No. 10/670,194

TC/A.U. 2811

Filed: September 26, 2003

Examiner: Im, Junghwa

For: CHIP-STACK SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD OF
THE SAME

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May 8, 2006

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

AMENDMENT AFTER FINAL

Do not enter
Jm2 5/22/06

Responsive to the Official Action dated February 9, 2006, please amend the
above-identified application as follows: